Remarks:

The Applicant's response to the Office Action of January 17, 2007 received a Notice of Non-compliant Amendment on January 10, 2008, to which this amendment addresses the outstanding issues regarding the Abstract and Specification. The issues regarding Claims rejections are referred to the previously filed amendment of July 17, 2007.

Objections to the Abstract

The Abstract stands objected as using superfluous language (i.e. "present invention includes" and containing an undefined acronym ("DSP"). The abstract should be limited to a single paragraph within the range of 50 to 250 words. The abstract has been amended to address these objections as follows:

~~The invention includes a~~ A method and apparatus for generating a wide instruction controlling at least one data processing resource, local to that data processing resource, by accessing a local wide instruction memory based upon a narrow instruction, to generate at least part of the wide instruction. The local wide instruction memory can be accessed on every instruction cycle to reconfigure the controlled data processing resource(s). The data processing resources preferably includes arithmetic resources acting on the logarithms of various data inputs to generate a spectrum of non-additive results. A preferred embodiment permits the narrow instruction to include a designator field, a first narrow field and a second narrow field. The designator field is used by the local wide instruction memories to select which of the first and second narrow fields to use in accessing the memory for controls of a specific resource. Use in a graphics shader with four datapath columns is shown. Another preferred use is shown in a Digital Signal Processor (DSP) is also shown.

Consequently, the Applicant requests that this objection be removed.

Objection to Declaration

The Declaration filed with this application has been objected to as stating U.S. Parent Application Number "10/155,512", which should be "10/155,502". The specification has been amended as follows:

Serial Number 10/276,41, docket number ARITH001US, filed Nov. 12, 2002, which is the national stage application based upon, Serial number PCT/US 01/15,541, entitled "Method and apparatus of DSP resource allocation and use", filed May 14, 2001 by Jennings, docket number ARITH001; ~~*and*~~

Serial number 10/226,735, entitled "Method and apparatus for high speed calculation of nonlinear functions and networks using non-linear function calculators in digital signal processing", docket number ARITH003, filed August 22, 2002; *and*

Serial number 10/155,502, filed May 23, 2002.

Consequently, the Applicant requests that this objection be removed.

Request for telephone interview if this application is not in condition for allowance

The Applicant believes that this application is in condition for allowance and requests that if the Examiner does not find this application in condition for allowance, that Earle Jennings be contacted for a telephone interview to discuss how to expedite the application's prosecution.

/Earle Jennings/
Earle Jennings
Patent Agent 44,804 Telephone: (510) 559-9074
8 Kenyon Ave. FAX: (510) 559-2970
Kensington, CA 94708 email: ewj@ix.netcom.com